

Nordic Semiconductor ASA

nPM2100 Evaluation Board (PCA10170)

Sheet 1: Cover Sheet

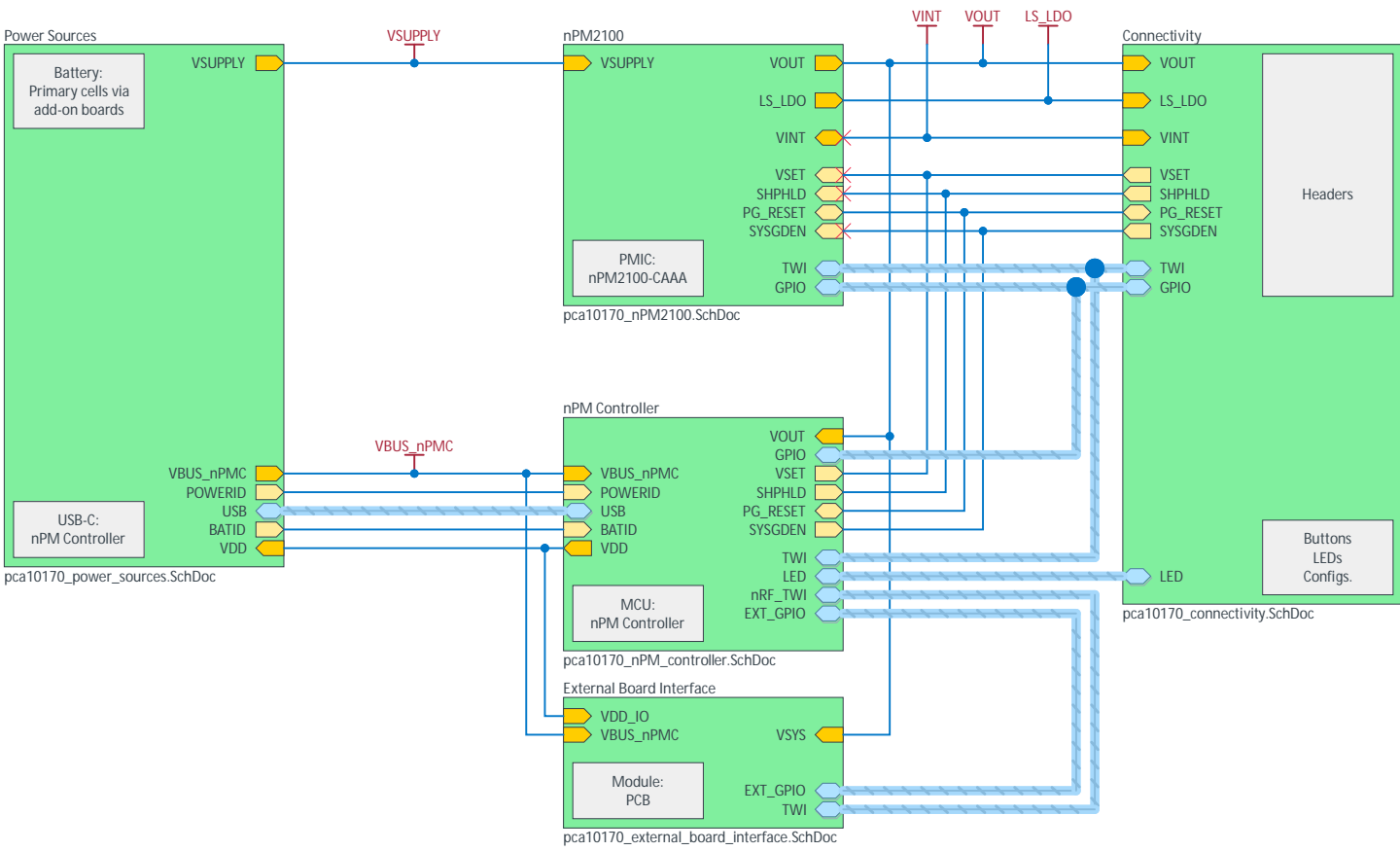
Sheet 2: nPM2100

Sheet 3: Power Sources

Sheet 4: Connectivity

Sheet 5: nPM Controller

Sheet 6: External Board Interface



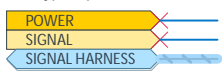
The schematics have following net naming hierarchy:
1) Power ports
2) Ports
3) Net labels

The top level sheet has highest priority.
Net labels are local to sheets.

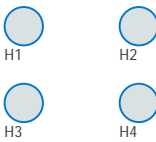
Alignment Fiducials



Port Type Explanation



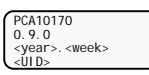
Mechanical holes



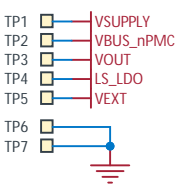
Bumpers



Label



Production Test Points



Title
nPM2100 Evaluation Board

Size
A3

PCB Assembly Number
PCA10170

Revision
0.9.0

Date: 25.02.2025

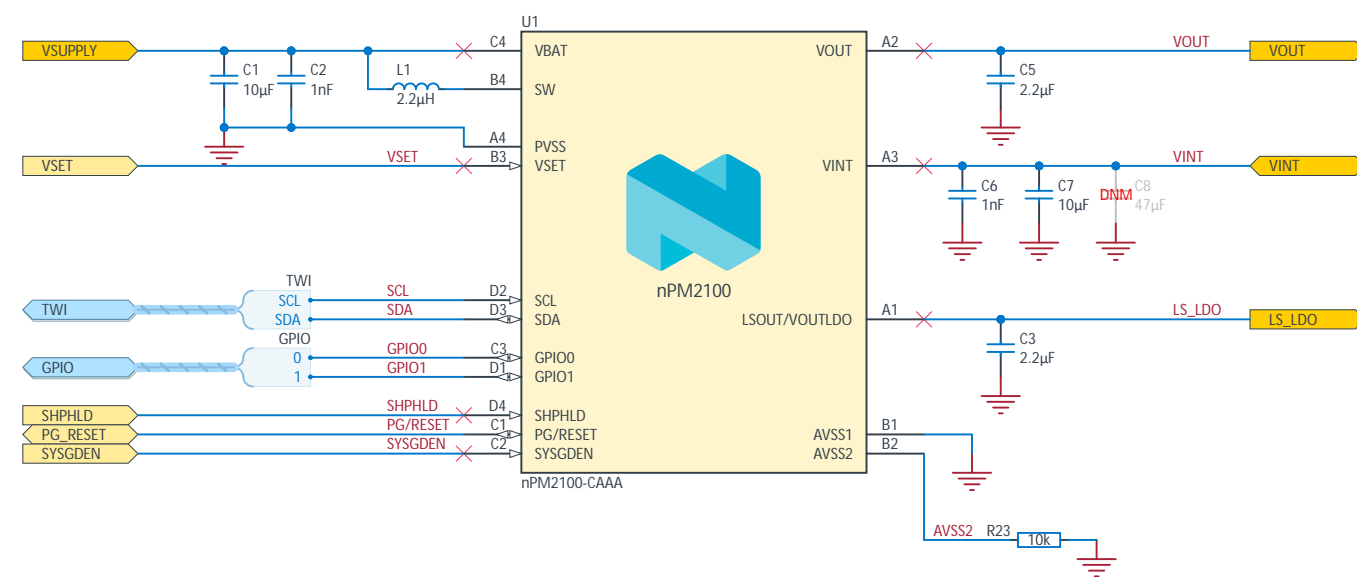
File: pca10170_cover_sheet.SchDoc

Classification: Public

Sheet 1 of 6

Drawn By: DABA






Production Test Points	
TP39	SHPHLD
TP40	PG/RESET
TP41	SYSGDEN
TP42	VSET
TP43	SCL
TP44	SDA
TP45	GPIO0
TP46	GPIO1
TP47	VINT

Internal Use	
TP48	VBUS_nPMC
TP49	AVSS2

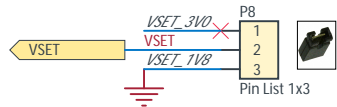
Title nPM2100 Evaluation Board: nPM2100 PMIC			
Size A3	PCB Assembly Number PCA10170	Revision 0.9.0	
Date: 25.02.2025		Sheet 2 of 6	
File: pca10170_nPM2100.SchDoc		Drawn By: DABA	
Classification: Public			

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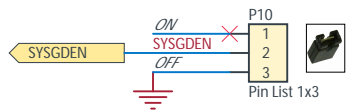
Title			
nPM2100 Evaluation Board: Power Sources			
Size	PCB Assembly Number	Revision	
A3	PCA10170	0.9.0	
Date: 07.01.2025	Sheet 3 of 6		
File: pca10170_power_sources.SchDoc		Drawn By: DABA	
Classification: Public			



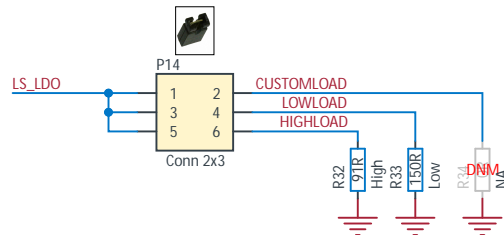
Boost Regulator Startup Voltage Configuration



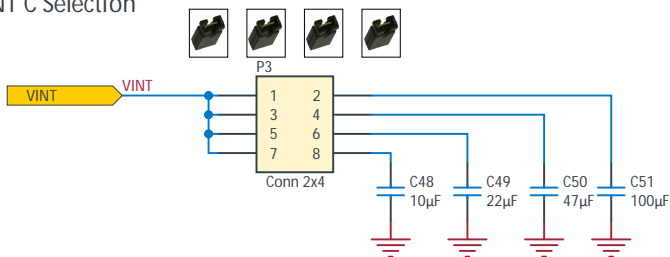
Boot monitor Control



Load Switch Configuration / LDO Bypass Capacitors



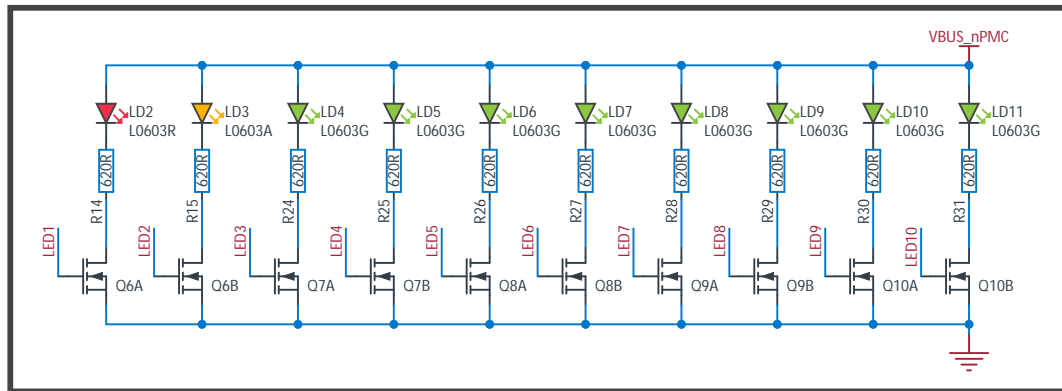
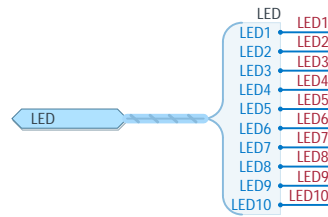
VINT C Selection



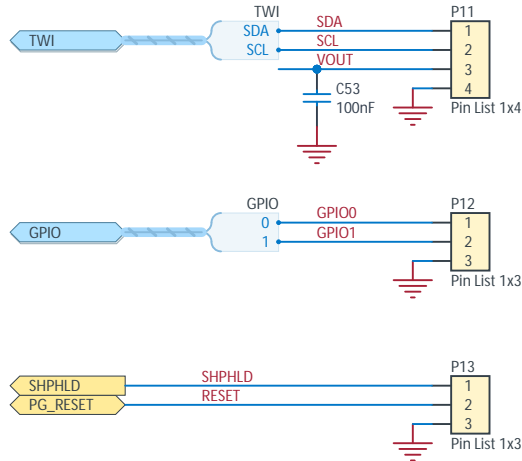
GND probe loops



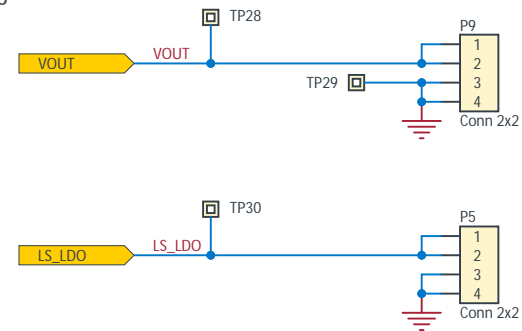
On-board fuel gauge LED strip



I/O Pins



Output Voltages



Buttons

