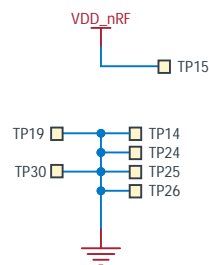
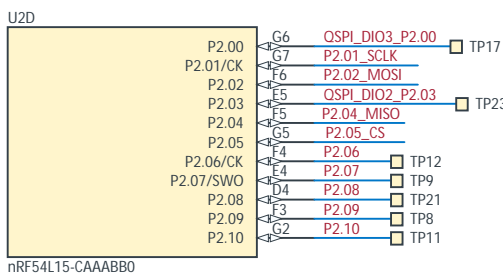
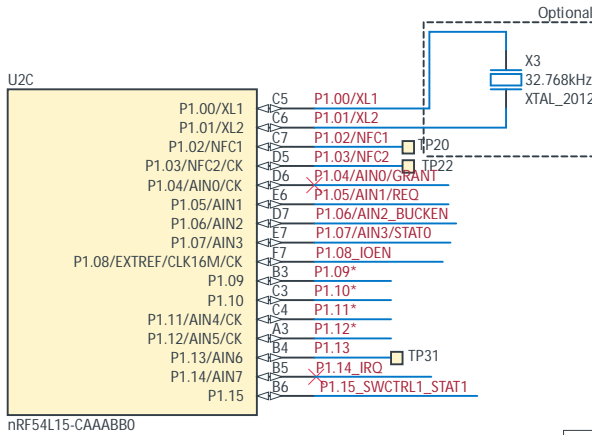
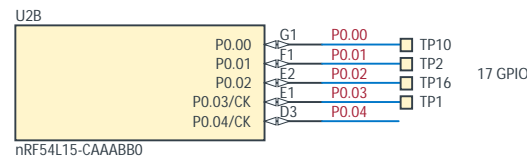
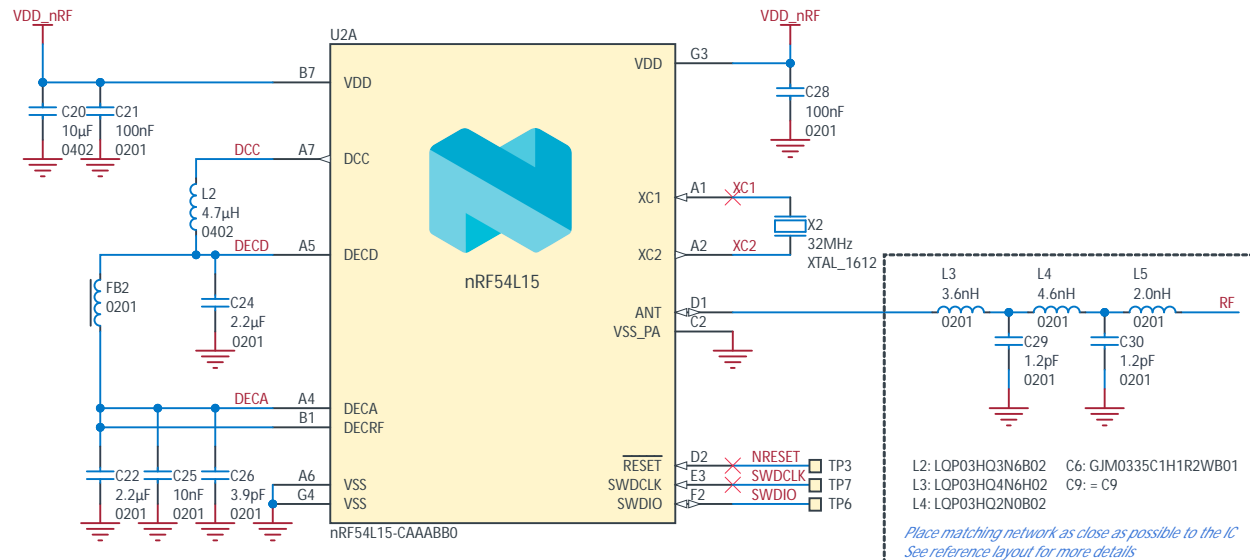
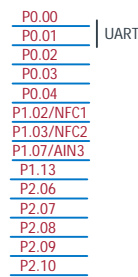


nRF54L15



* Please refer to chip-level errata for GPIO pins P1.09, P1.10, P1.11 and P1.12: they are open and not used.

Customer IO's



nRF7002

