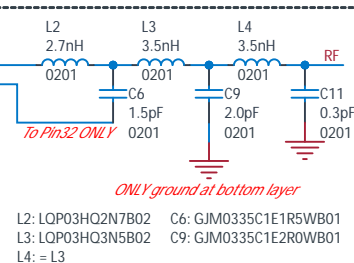
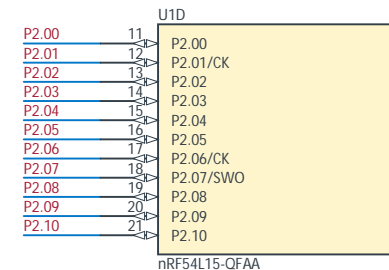
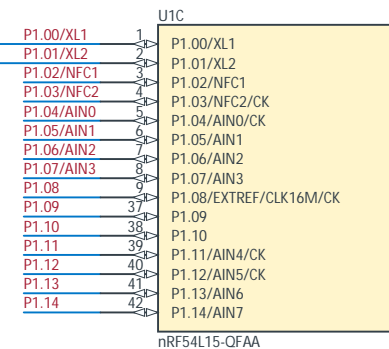
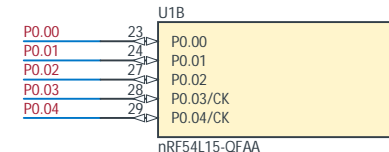
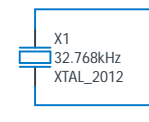


Optional



Important note on C6:

- Grounding of C6 capacitor must ONLY be connected to Pin32 (VSS_PA) in top layer.
- Pin32 must ONLY be connected to Pin49 (VSS center pad) underneath the IC package

Important note on C9:

- Grounding of C9 capacitor MUST be isolated from ALL ground layers except the bottom ground layer.

Place matching network as close as possible to the IC
See reference layout for more details

Title nRF54L15-qfaa Reference Layout

Size
A4

PCB Assembly Number
nRF54L15-qfaa Reference Layout

Revision
v0.8



Date: 04.02.2025
File: nRF54L15_qfaa_4L_config1.SchDoc
Classification: PUBLIC

Sheet 1 of 1
Drawn By: STL1