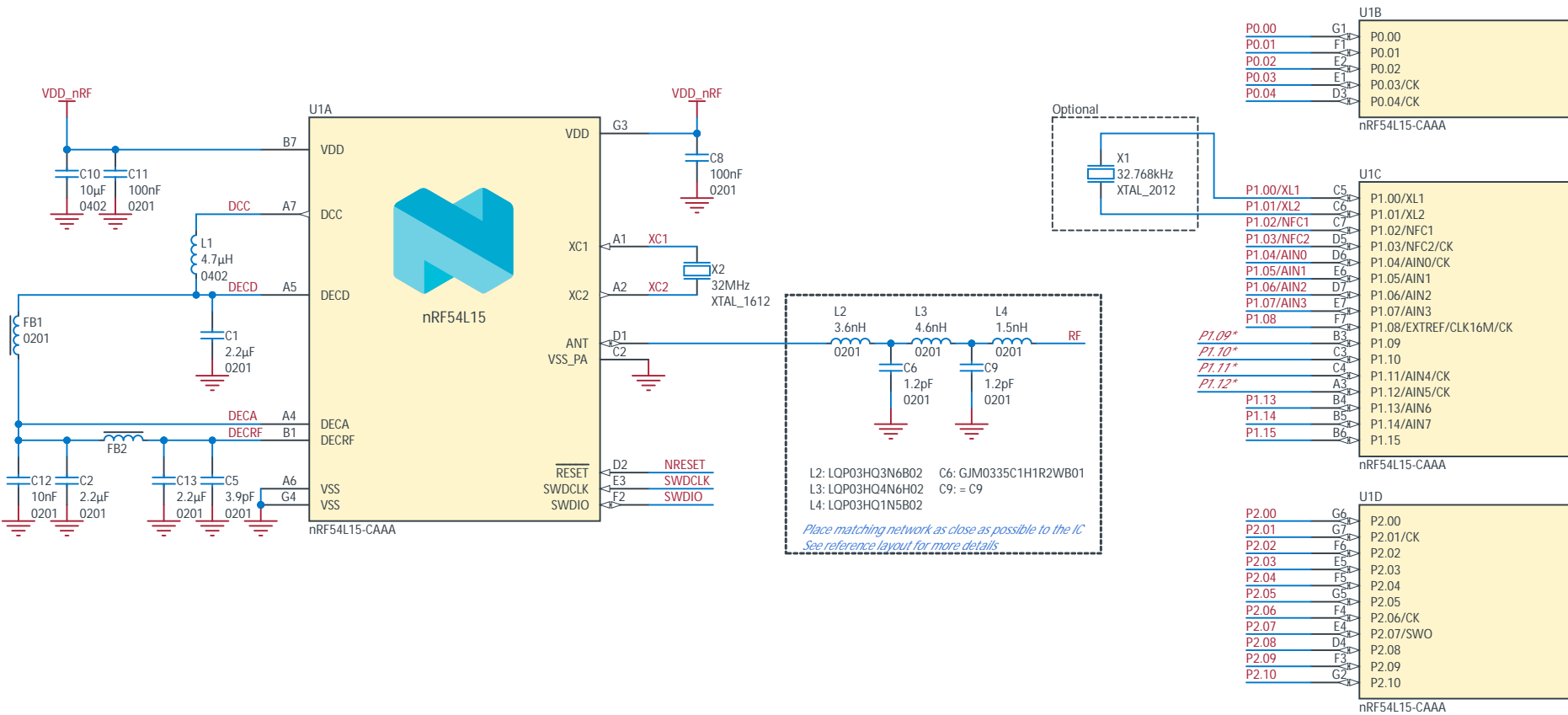


# Reference Layout for nRF54L15-CAAA

Config 1: Battery/External regulator, Disabled NFC



\* Please refer to chip-level errata before using GPIO pins P1.09, P1.10, P1.11 and P1.12

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Reference Layout Title

Battery/External regulator, Disabled NFC

Size	Config. No.	Device	Revision
A4	1	nRF54L15-CAAA	0.5
Date:	09.05.2025	Sheet 1 of 1	
File:	nRF54L15_caaa_config1.SchDoc	Drawn By:	STL/DABA
Classification:	Public		

